

FIG. 1

K1[Mode] bits 7~4	K1[CH] bits 3~0	K2[CH] bits 7~4	K2[S] bits 3	K2[STS] bits 2~0
request type	switching requested channel number	switching channel number	protection channel number	switching status

FIG. 2

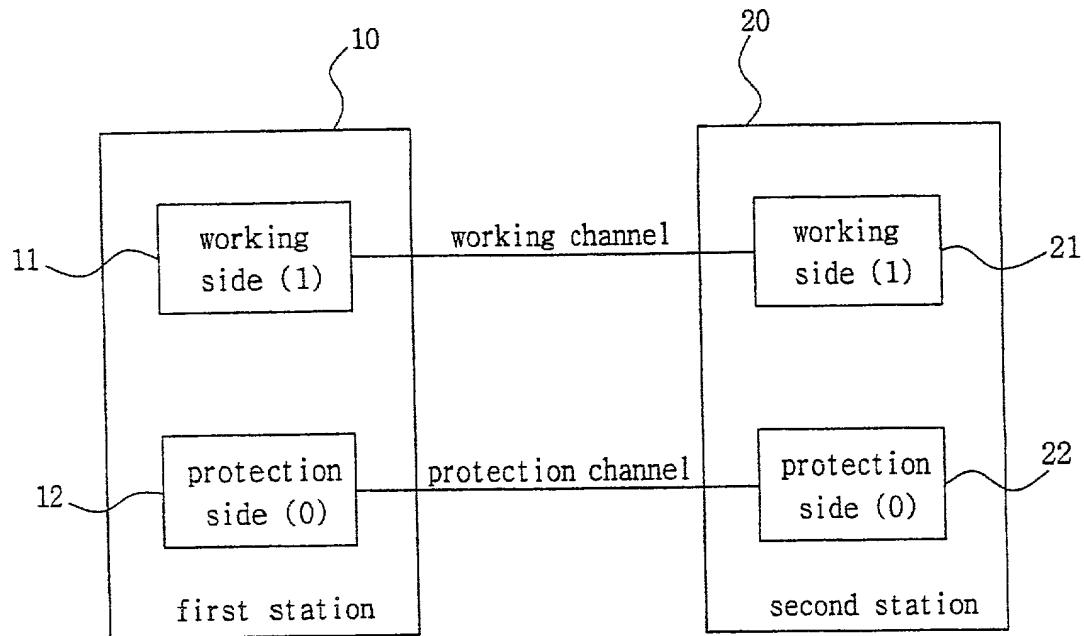


FIG. 3

channel value for request from working side to protection side (ATOB)	X10X
channel value for request from protection side to working side (BTOA)	X01X
channel value for working side in active mode (AWORK)	X00X
channel value for protection side in active mode (BWORK)	X11X

4 0 0 5 2 5 4 4 2 2 0 0 4

FIG. 4

(PRIOR ART)

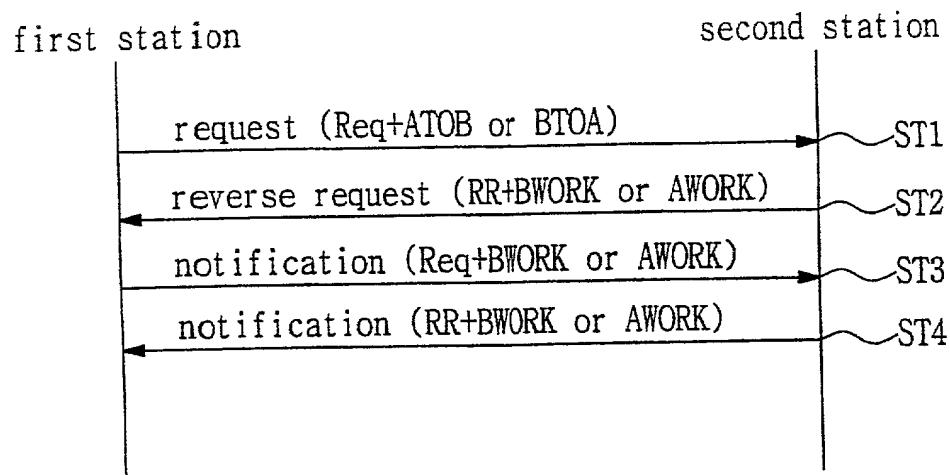


FIG. 5

